

Appln. No.: 10/540,606  
Response Dated: August 26, 2008  
Reply to Office Action of August 13, 2008

MAT-8716US

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Appln. No: 10/540,606  
Applicant: Toshiaki Takenaka et al.  
Filed: June 27, 2005  
Title: METHOD AND APPARATUS FOR MANUFACTURING CIRCUIT  
BOARD  
TC/A.U.: 1792  
Examiner: Brian K. Talbot  
Confirmation No.: 9266  
Docket No.: MAT-8716US

**RESPONSE TO RESTRICTION REQUIREMENT**

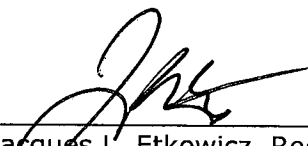
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

S I R :

This is in response to the Restriction Requirement stated in the Office  
Letter dated **August 13, 2008**.

The Examiner requires that claims of either Group I or Group II be  
elected for prosecution. Applicants elect to prosecute Group I, claims 1-16 and 23-  
35, drawn to method. This election is made without traverse.

Respectfully submitted,

  
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Dated: August 26, 2008  
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